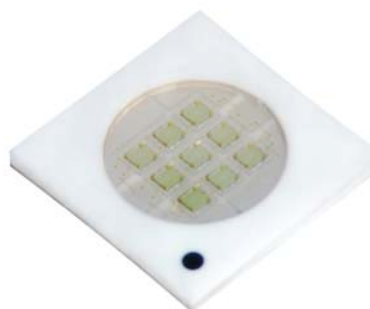




ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELECTROSTATIC
DISCHARGE
SENSITIVE
DEVICES

Part Number: KT-1213WG9SX9/10



Features

- Dimensions : 12mmX12mmX1.2mm
- High power lighting.
- Super high flux output and high luminance.
- Designed for high current operation.
- Low thermal resistance.
- Soldering methods: IR reflow soldering.
- Moisture sensitivity level : level 2a.
- RoHS compliant.

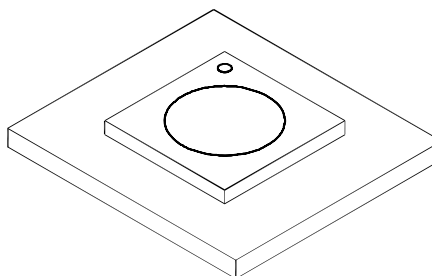
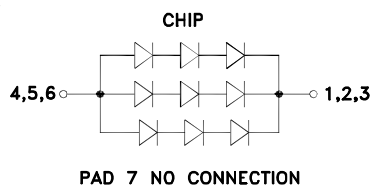
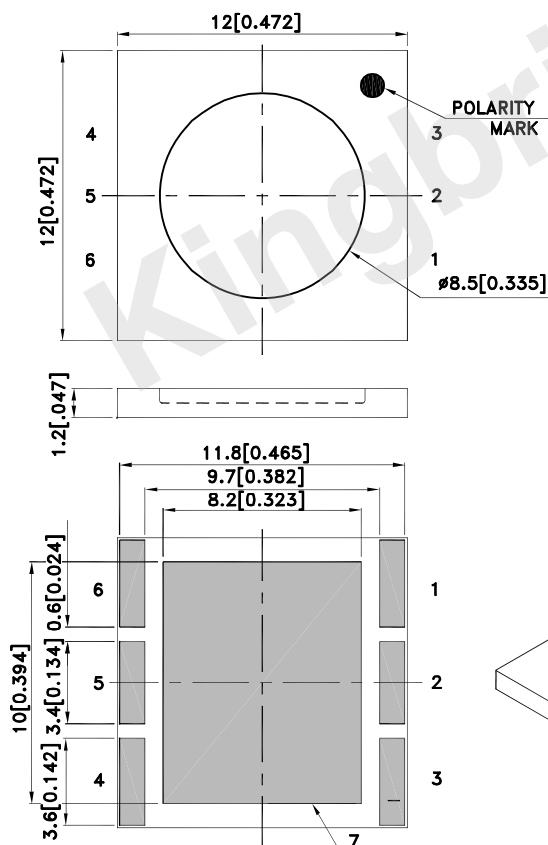
Materials:

Package : Ceramics

Encapsulating resin : Silicone resin

Electrodes : Ag plating

Package Dimensions



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25(0.01")$ unless otherwise noted.
3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.
4. The device has a single mounting surface. The device must be mounted according to the specifications.



Selection Guide

Part No.	Dice	Lens Type	Φ_v (lm) [2] @ 1A		Viewing Angle [1]
			Min.	Typ.	2 θ 1/2
KT-1213WG9SX9/10	Green (InGaN)	Water Clear	370	460	120°

Notes:

1. θ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity/ luminous Flux: +/-15%.
3. Luminous Flux value is traceable to the CIE127-2007 compliant national standards.

Absolute Maximum Ratings at TA=25°C

Parameter	Symbol	Value	Unit
Power dissipation	P _D	11.5	W
Junction temperature[1]	T _J	120	°C
Operating Temperature	T _{op}	-40 To +100	°C
Storage Temperature	T _{stg}	-40 To +120	°C
DC Forward Current [1]	I _F	1	A
Peak Forward Current [2]	I _{FM}	1.2	A
Reverse Voltage	V _R	15	V
Thermal Resistance [1] (Junction/ambient)	R _{th j-a}	35	°C/W
Thermal Resistance [1] (Junction/substrate)	R _{th j-S}	6	°C/W
Electrostatic Discharge Threshold (HBM)		2000	V

Notes:

1. Results from mounting on metal core PCB (25.4*25.4*1.6mm).
2. 1/10 Duty Cycle, 0.1ms Pulse Width.

Electrical / Optical Characteristics at TA=25°C

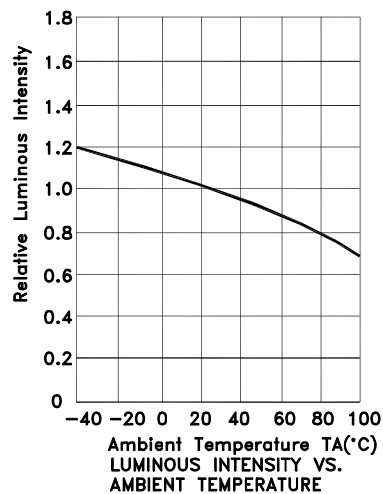
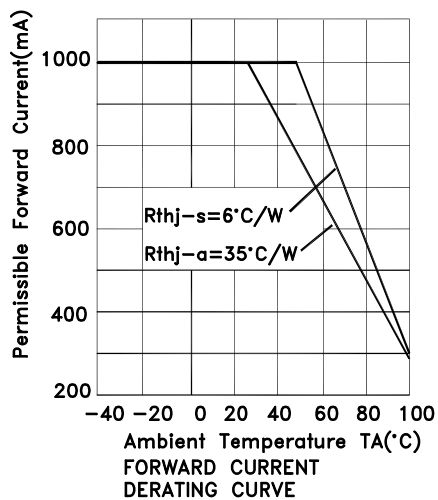
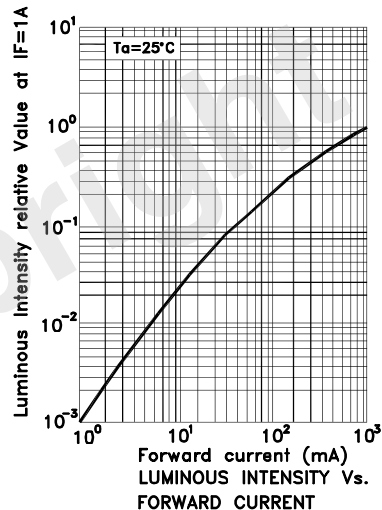
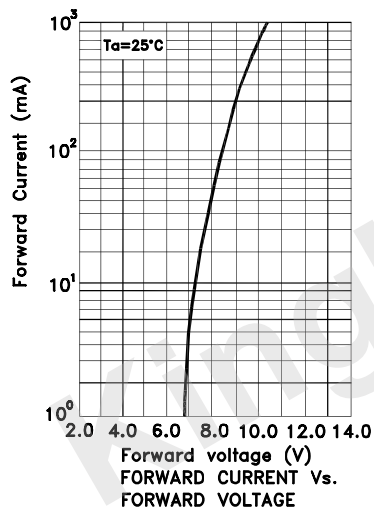
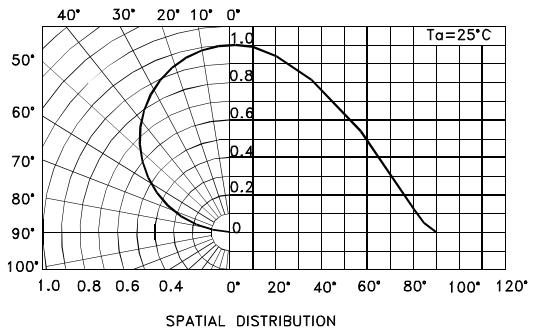
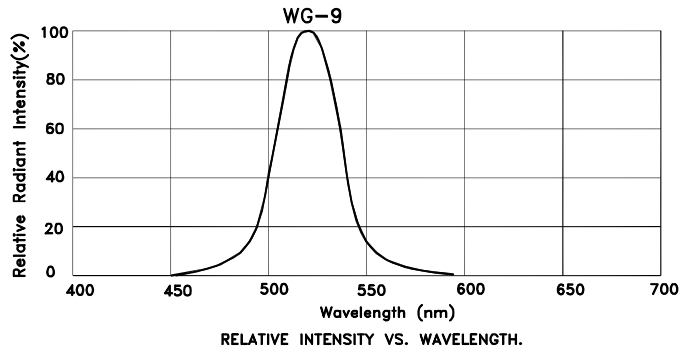
Parameter	Symbol	Value	Unit
Wavelength at peak emission I _F =1A [Typ.]	λ peak	520	nm
Dominant Wavelength I _F =1A [Typ.]	λ dom [1]	525	nm
Spectral bandwidth at 50% $\Phi_{REL MAX}$ I _F =1A [Typ.]	$\Delta\lambda$	35	nm
Reverse Current (V _R = 15V)	I _R	30	uA
Forward Voltage I _F =1A [Min.]	V _F [2]	9.3	V
Forward Voltage I _F =1A [Typ.]		10.4	
Forward Voltage I _F =1A [Max.]		11.5	
Temperature coefficient of λ peak I _F =1A, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TC λ peak	0.07	nm/° C
Temperature coefficient of λ dom I _F =1A, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TC λ dom	0.05	nm/° C
Temperature coefficient of V _F I _F =1A, -10 ° C ≤ T ≤ 100 ° C [Typ.]	TC _V	-5.2	mV/° C

Notes:

1. Wavelength : + / -1nm.
2. Forward Voltage: +/-0.1V.
3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

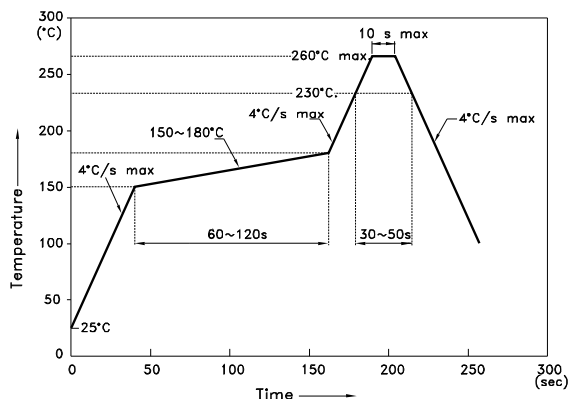
Green

KT-1213WG9SX9/10

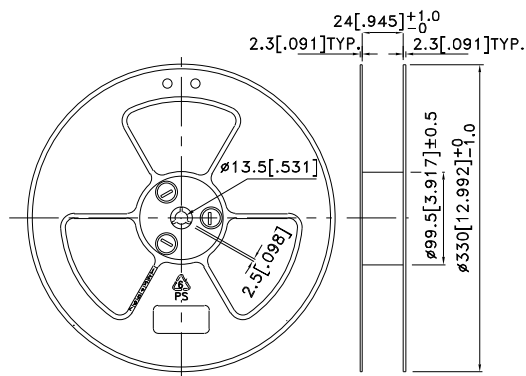


Reflow soldering is recommended and the soldering profile is shown below. Other soldering methods are not recommended as they might cause damage to the prod-

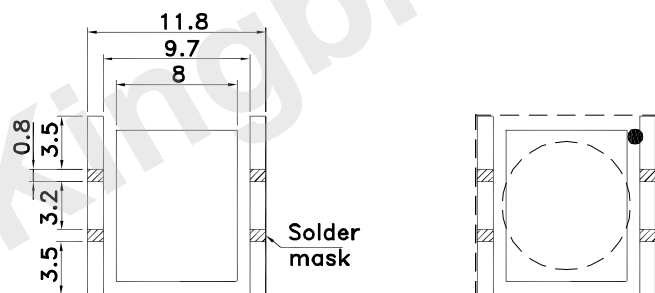
Reflow Soldering Profile For Lead-free SMT Process.



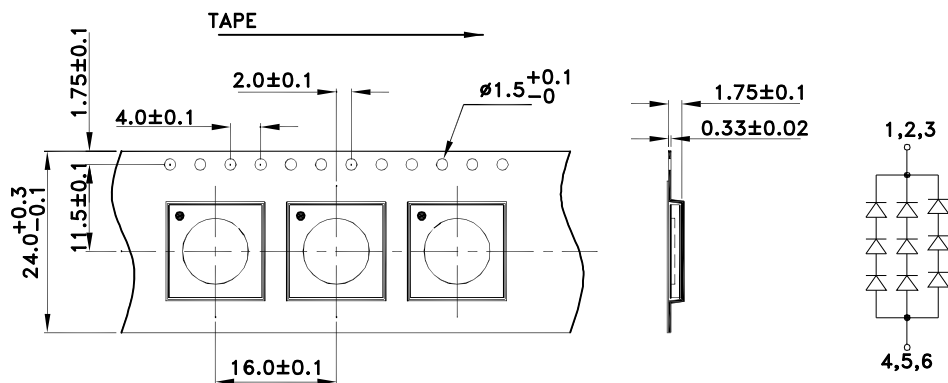
Reel Dimension



**Aluminum PCB Design
Recommended Soldering Pattern
(Units : mm ; Tolerance: ± 0.1)**

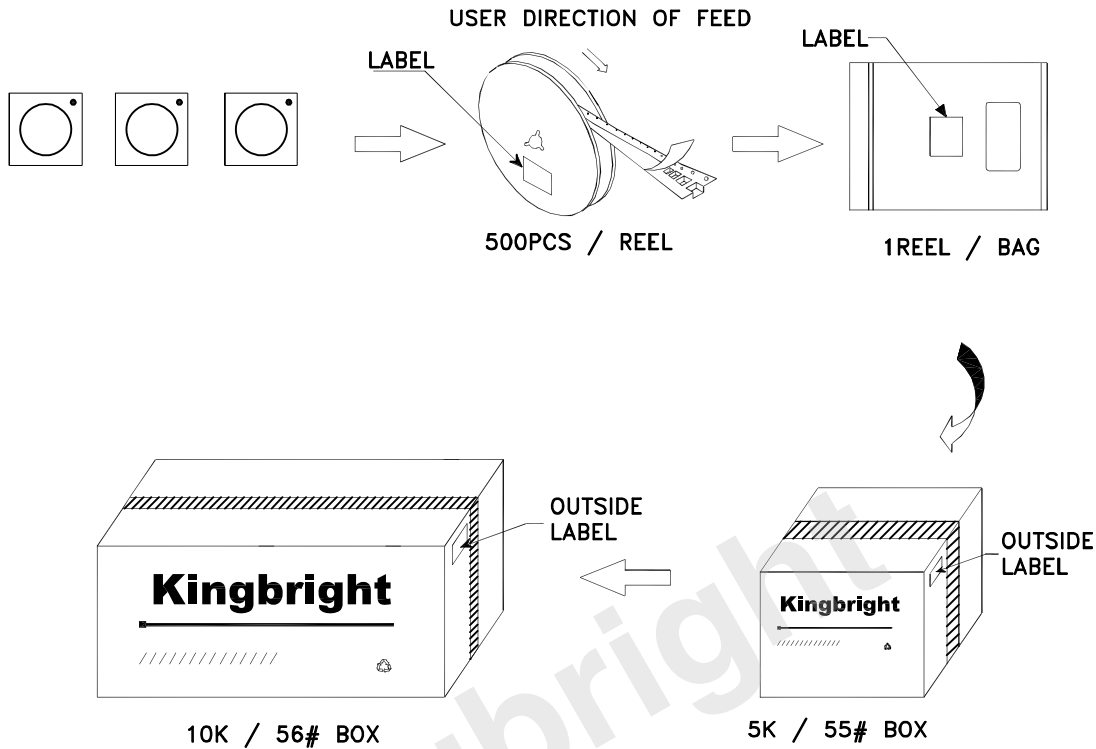



Tape Dimensions (Units : mm)



PACKING & LABEL SPECIFICATIONS

KT-1213WG9SX9/10



Kingbright		
P/NO: KT-1213xxx		
QTY: 500 pcs	Q.C.	Q C
S/N: XXXX		xx xx xxxx
CODE: CIE:XXX		PASSED
LOT NO:		
		
RoHS Compliant		

Packaging:

- 1.The LEDs are packed in cardboard boxes after taping.
- 2.The label on the minimum packing unit shows: Part Number, Lot Number, Ranking, Quantity.
- 3.In order to protect the LEDs from mechanical shock, we pack them in cardboard boxes for transportation.
- 4.The LEDs may be damaged if the boxes are dropped or receive a strong impact against them, so precautions must be taken to prevent any damage.
- 5.The boxes are not water resistant and therefore must be kept away from water and moisture.
- 6.When the LEDs are transported, we recommend that you use the same packing methods as Kingbright's.

JEDEC Moisture Sensitivity:

Level	Floor Life		Soak Requirements			
			Standard		Accelerated Equivalent	
	Time	Conditions	Time (hours)	Conditions	Time (hours)	Conditions
2a	4 weeks	$\leq 30^{\circ}\text{C} / 60\% \text{ RH}$	696^2 $+ 5 / - 0$	$30^{\circ}\text{C} / 60\% \text{ RH}$	120 $+ 1 / - 0$	$60^{\circ}\text{C} / 60\% \text{ RH}$

Notes:

- CAUTION - The "accelerated equivalent" soak requirements **shall not** be used until correlation of damage response, including electrical, after soak and reflow is established with the "standard" soak requirements or if the known activation energy for diffusion is 0.4 - 0.48 eV. Accelerated soak times may vary due to material properties, e.g., mold compound, encapsulant, etc. JEDEC document JESD22-A120 provides a method for determining the diffusion coefficient.
- The standard soak time includes a default value of 24 hours for semiconductor manufacturer's exposure time (MET) between bake and bag and includes the maximum time allowed out of the bag at the distributor's facility.
If the actual MET is less than 24 hours the soak time may be reduced. For soak conditions of $30^{\circ}\text{C}/60\% \text{ RH}$ the soak time is reduced by one hour for each hour the MET is less than 24 hours. For soak conditions of $60^{\circ}\text{C}/60\% \text{ RH}$, the soak time is reduced by one hour for each five hours the MET is less than 24 hours.
If the actual MET is greater than 24 hours the soak time must be increased. If soak conditions are $30^{\circ}\text{C}/60\% \text{ RH}$, the soak time is increased one hour for each hour that the actual MET exceeds 24 hours. If soak conditions are $60^{\circ}\text{C}/60\% \text{ RH}$, the soak time is increased one hour for each five hours that the actual MET exceeds 24 hours.
- Supplier may extend the soak times at their own risk.

ESD Protection During Production

Electric static discharge can result when static-sensitive products come in contact with the operator or other conductors.

The following procedures may decrease the possibility of ESD damage:

- Minimize friction between the product and surroundings to avoid static buildup.
- All production machinery and test instruments must be electrically grounded.
- Operators must wear anti-static bracelets.
- Wear anti-static suit when entering work areas with conductive machinery.
- Set up ESD protection areas using grounded metal plating for component handling.
- All workstations that handle IC and ESD-sensitive components must maintain an electrostatic potential of 150V or less.
- Maintain a humidity level of 50% or higher in production areas.
- Use anti-static packaging for transport and storage.
- All anti-static equipment and procedures should be periodically inspected and evaluated for proper functionality.

Heat Generation:

- Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board, as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.
- Please determine the operating current with consideration of the ambient temperature local to the LED and refer to the plot of Permissible Forward current vs. Ambient temperature on CHARACTERISTICS in this specification. Please also take measures to remove heat from the area near the LED to improve the operational characteristics on the LED.

- The equation ① indicates correlation between T_j and T_a , and the equation ② indicates correlation between T_j and T_s

$$T_j = T_a + R_{thj-a} * W \quad \text{①}$$

$$T_j = T_s + R_{thj-s} * W \quad \text{②}$$

T_j = dice junction temperature: $^{\circ}\text{C}$

T_a = ambient temperature: $^{\circ}\text{C}$

T_s = solder point temperature: $^{\circ}\text{C}$

R_{thj-a} = heat resistance from dice junction temperature to ambient temperature : $^{\circ}\text{C} / \text{W}$

R_{thj-s} = heat resistance from dice junction temperature to T_s measuring point : $^{\circ}\text{C} / \text{W}$

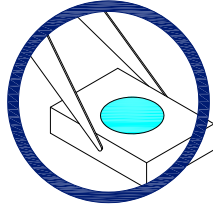
W = inputting power (IFx VF) : W

Handling Precautions

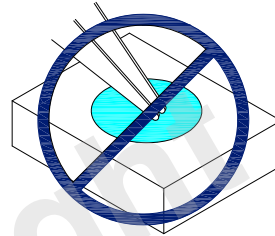
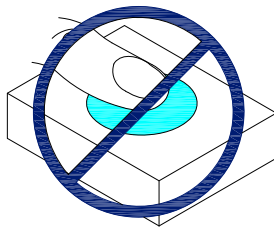
Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force.

As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might lead to damage and premature failure of the LED.

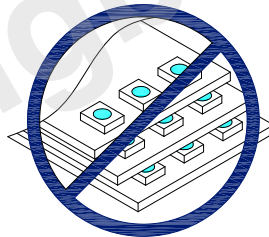
1. Handle the component along the side surfaces by using forceps or appropriate tools.



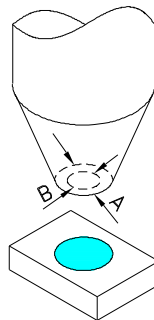
2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.



3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



- 4.1. The inner diameter of the SMD pickup nozzle should not exceed the size of the LED to prevent air leaks.
- 4.2. A pliable material is suggested for the nozzle tip to avoid scratching or damaging the LED surface during pickup.
- 4.3. The dimensions of the component must be accurately programmed in the pick-and-place machine to insure precise pickup and avoid damage during production.



5. As silicone encapsulation is permeable to gases, some corrosive substances such as H_2S might corrode silver plating of leadframe. Special care should be taken if an LED with silicone encapsulation is to be used near such substances.

Detailed application notes are listed on our website.

http://www.kingbright.com/application_notes