

Part Number: L-43HD

Bright Red

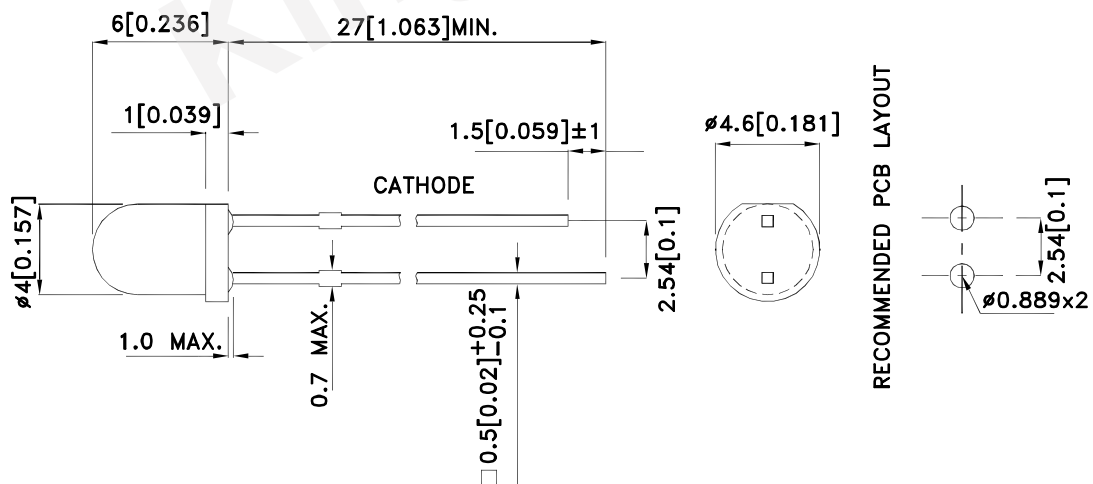
Features

- Low power consumption.
- Versatile mounting on P.C. board or panel.
- Popular 4mm diameter.
- Reliable and rugged.
- RoHS compliant.

Description

The Bright Red source color devices are made with Gallium Phosphide Red Light Emitting Diode.

Package Dimensions



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is ± 0.25 (0.01") unless otherwise noted.
3. Lead spacing is measured where the lead emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



Selection Guide

Part No.	Emitting Color (Material)	Lens Type	Iv (mcd) [2] @ 10mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
L-43HD	Bright Red (GaP)	Red Diffused	0.8	2	80°

Notes:

1. $\theta_{1/2}$ is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity / luminous Flux: $\pm 15\%$.
3. Luminous intensity value is traceable to CIE127-2007 standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Emitting Color	Typ.	Max.	Units	Test Conditions
λ_{peak}	Peak Wavelength	Bright Red	700		nm	I _F =10mA
λ_D [1]	Dominant Wavelength	Bright Red	635		nm	I _F =10mA
$\Delta\lambda_{1/2}$	Spectral Line Half-width	Bright Red	45		nm	I _F =10mA
C	Capacitance	Bright Red	40		pF	V _F =0V; f=1MHz
V _F [2]	Forward Voltage	Bright Red	2.05	2.5	V	I _F =10mA
I _R	Reverse Current	Bright Red		10	uA	V _R = 5V

Notes:

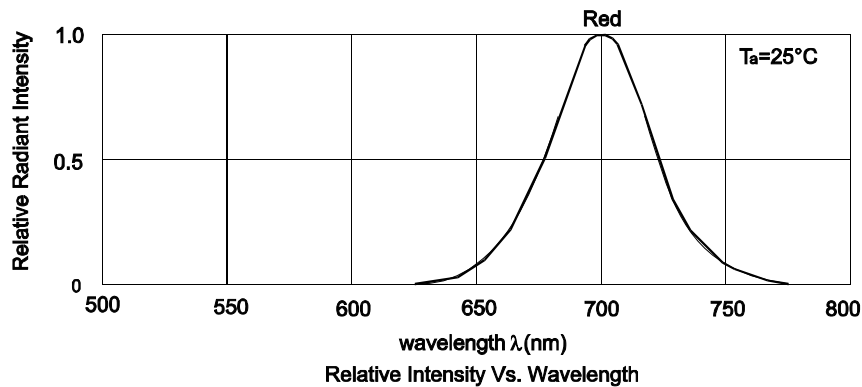
1. Wavelength: $\pm 1\text{nm}$.
2. Forward Voltage: $\pm 0.1\text{V}$.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

Absolute Maximum Ratings at TA=25°C

Parameter	Values	Units
Power dissipation	62.5	mW
DC Forward Current	25	mA
Peak Forward Current [1]	130	mA
Reverse Voltage	5	V
Operating/Storage Temperature	-40°C To +85°C	
Lead Solder Temperature [2]	260°C For 3 Seconds	
Lead Solder Temperature [3]	260°C For 5 Seconds	

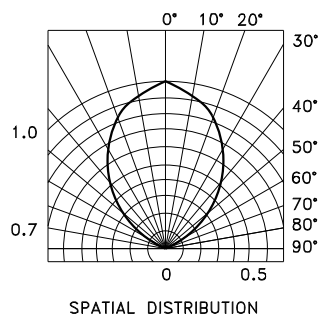
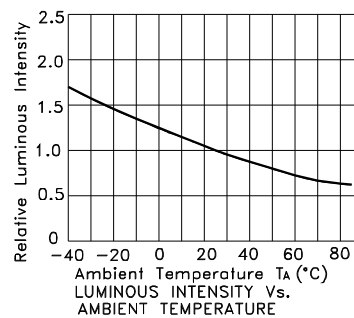
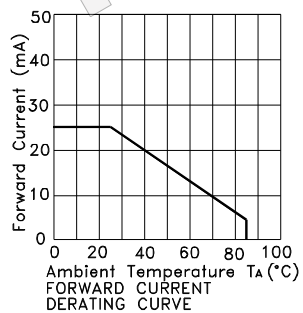
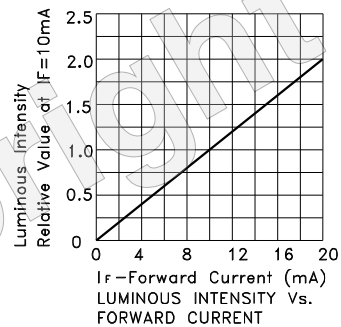
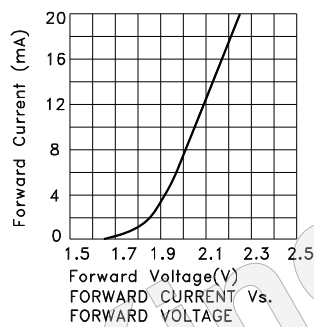
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. 2mm below package base.
3. 5mm below package base.
4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.



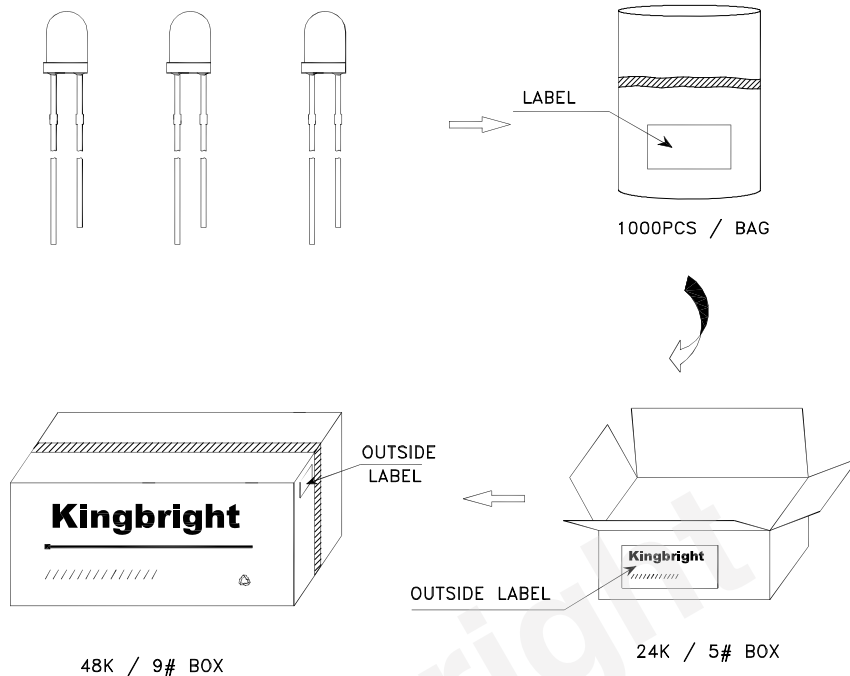
Bright Red


L-43HD



PACKING & LABEL SPECIFICATIONS

L-43HD



Kingbright	
P/NO: L-43xxx	
QTY: 1000 pcs	Q.C. Q C XX-XX-XXXX PASSED
S/N: XXXX	
CODE: XXX	
LOT NO:	
	
RoHS Compliant	

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PRECAUTIONS

1. Storage conditions:

- Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- LEDs should be stored with temperature $\leq 30^{\circ}\text{C}$ and relative humidity $< 60\%$.
- Product in the original sealed package is recommended to be assembled within 72 hours of opening.
Product in opened package for more than a week should be baked for 30 (+10/-0) hours at $85 \sim 100^{\circ}\text{C}$.

2. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)

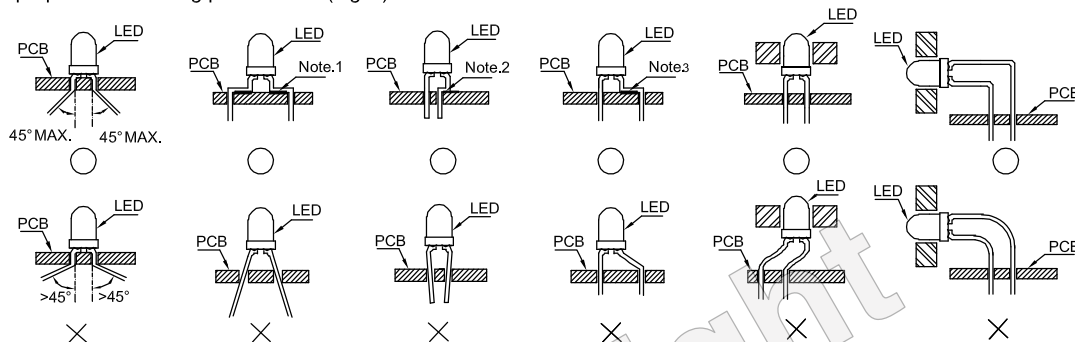


Fig.1

"○" Correct mounting method "×" Incorrect mounting method

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

3. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure. (Fig. 2)

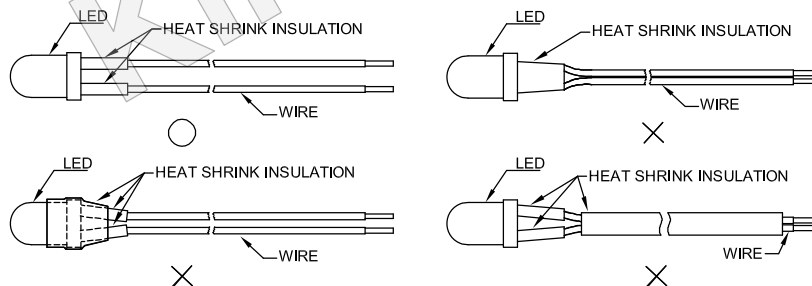


Fig. 2

4. Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

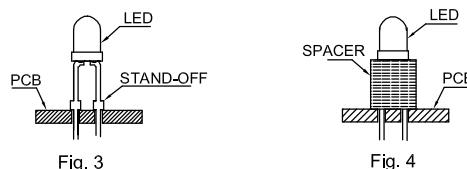


Fig. 3

Fig. 4

5. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)

6. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

