

Part Number: KTIR0911S

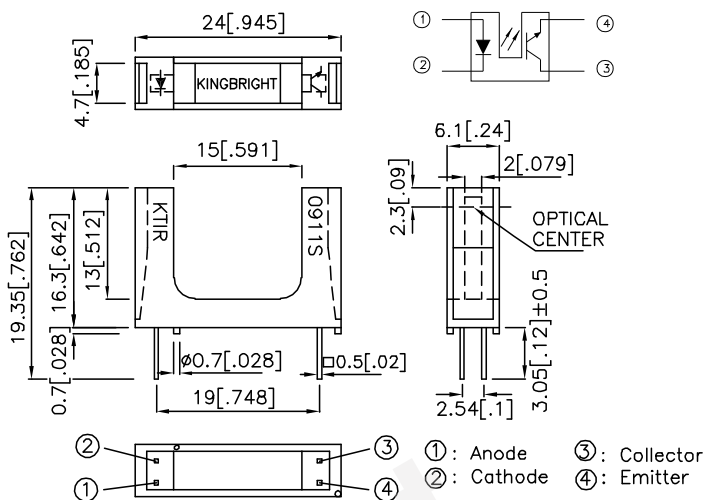
Package Dimensions

Features

- Ultra-Small.
- Minimal influence from stray light.
- Low collector-emitter saturation Voltage.
- RoHS compliant.

Applications

- Optical control equipment.
- Cameras.
- Floppy disk drives.



Notes:
 1. All dimensions are in millimeters (inches).
 2. Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
 3. Lead spacing is measured where the leads emerge from the package.
 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

Absolute Maximum Ratings(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward current	IF	50	mA
	Reverse voltage	VR	6	V
	Power dissipation	Pd	75	mW
	Peak Forward Current (Pulse Width <100uS,Duty Cycle=1%)	IFP	1	A
Output	Collector-emitter voltage	VCEO	35	V
	Emitter-collector voltage	VECO	6	V
	Collector current	IC	20	mA
	Collector power dissipation	PC	75	mW
Operating temperature		Topr	-25~+85	°C
Storage temperature		Tstg	-40~+100	°C
Soldering temperature (1/16 inch from body for 5 seconds)		Tsol	260	°C

Electro-optical Characteristics(Ta=25°C)

Parameter		Symbol	Conditions	Min.	Typ.	Max.	Unit
Input	Forward voltage	V _F	I _F =20mA	1.0	1.2	1.5	V
	Reverse current	I _R	V _R =6V	—	—	10	μA
	Peak Wavelength	λ _P	I _F =20mA	—	940	—	nm
Output	Collector dark current	I _{CEO}	V _{CE} =20V	—	—	100	nA
Transfer Characteristics	Collector-emitter saturation voltage		V _{CE(SAT)}	I _C =1mA I _F =40mA	—	0.4	V
	Current transfer ratio		CTR	V _{CE} =5V I _F =20mA	—	9.5	%
	Response time	Rise time	t _r	V _{CE} =2V I _C =2mA R _L =100Ω	—	5	μSec
		Fall time	t _f		—	4	μSec

Fig.1 FORWARD CURRENT Vs. FORWARD VOLTAGE

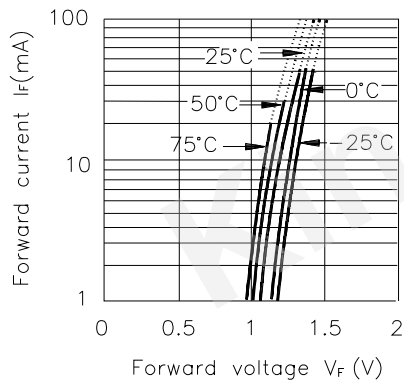


Fig.2 COLLECTOR CURRENT Vs. FORWARD CURRENT

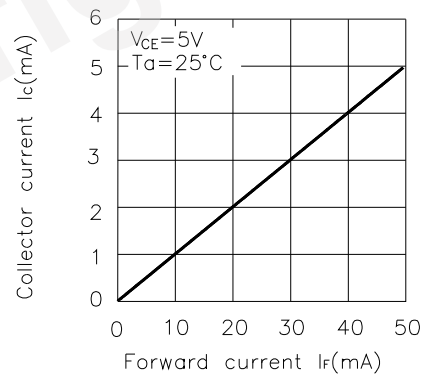


Fig.3 COLLECTOR CURRENT VS. COLLECTOR-EMITTER VOLTAGE

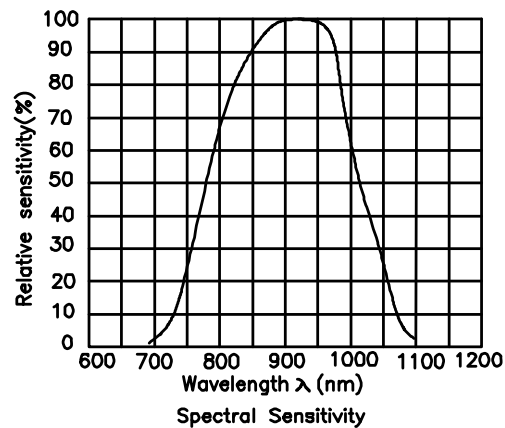
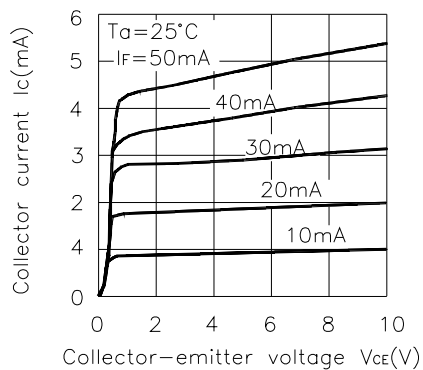


Fig.4 COLLECTOR CURRENT Vs. AMBIENT TEMPERATURE

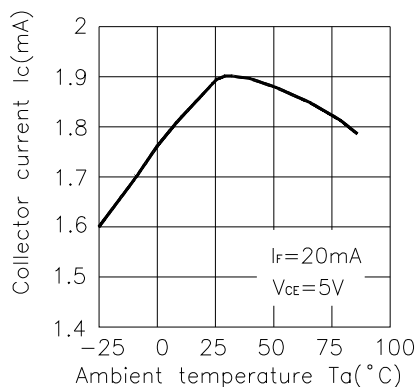


Fig.5 COLLECTOR-EMITTER SATURATION VOLTAGE Vs. AMBIENT TEMPERATURE

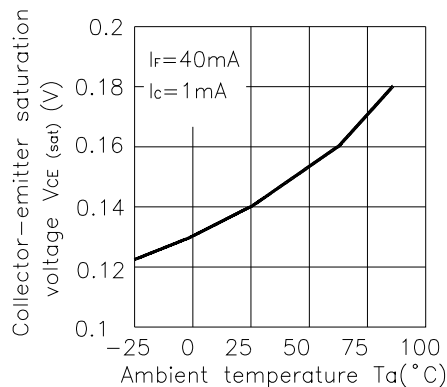


Fig.6 RELATIVE COLLECTOR CURRENT Vs. SHIELD DISTANCE (1)

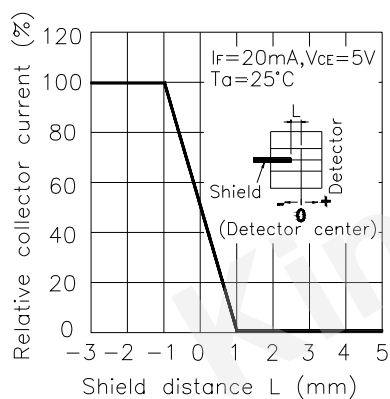


Fig.7 RELATIVE COLLECTOR CURRENT Vs. SHIELD DISTANCE (2)

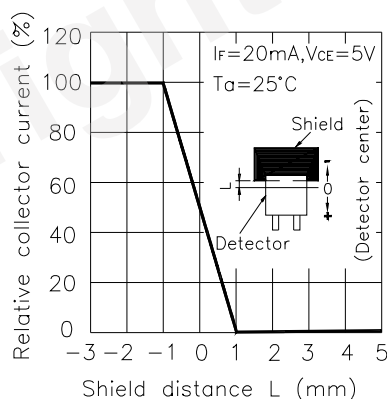
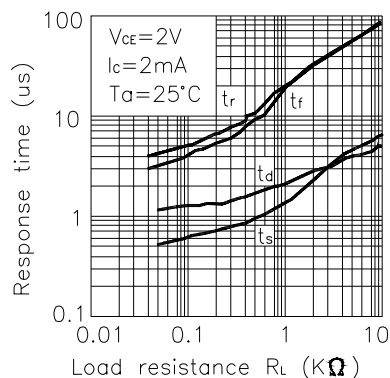
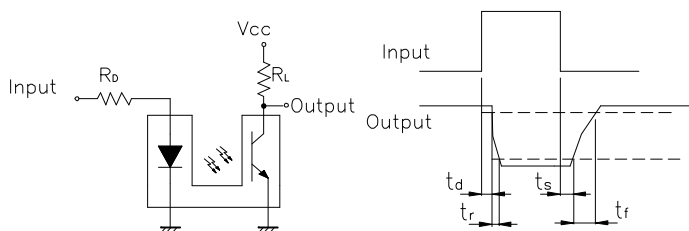


Fig.8 RESPONSE TIME Vs. LOAD RESISTANCE

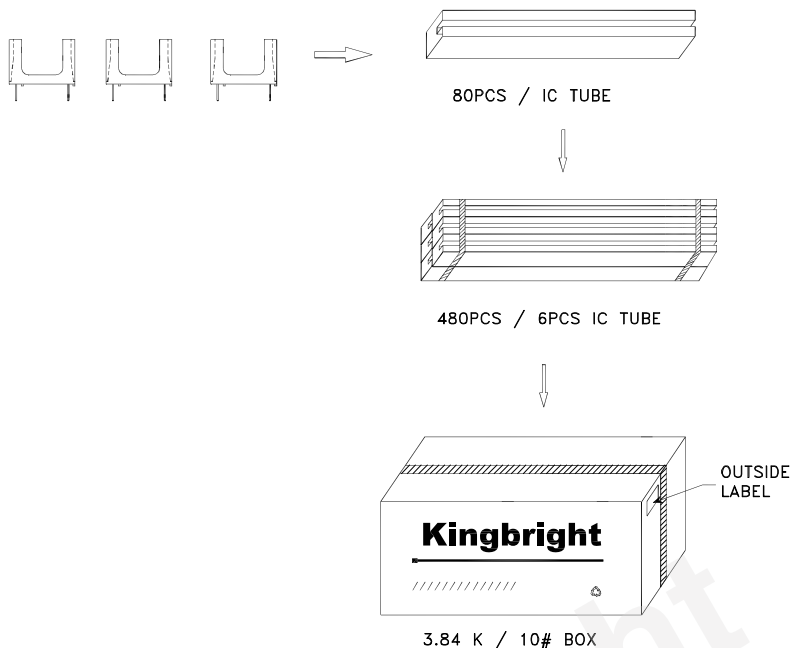


Test Circuit for Response Time



PACKING & LABEL SPECIFICATIONS

KTIR0911S



Kingbright	
P/NO: KTIRxxx	
QTY: 480 pcs	Q.C.
S/N: XXXX	Q C XX XX XXXX PASSED
CODE: XXX	
LOT NO:	
XXXXXXXXXXXXXXXXXX	
RoHS Compliant	

Terms and conditions for the usage of this document

- 1.The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- 2.The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- 3.When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
- 4.The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
- 5.The contents and information of this document may not be reproduced or re-transmitted without permission by Kingbright.
- 6.All design applications should refer to Kingbright application notes available at http://www.kingbright.com/application_notes