

Part Number: KP-3216P3C

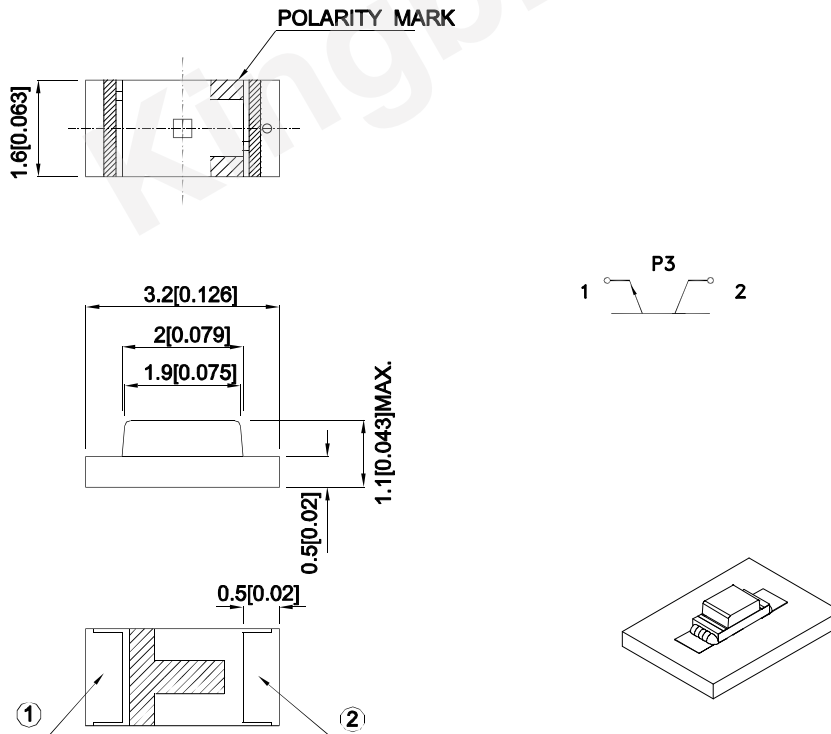
Features

- 3.2mmx1.6mm SMD LED, 1.1mm thickness.
- Mechanically and spectrally matched to infrared emitting LED lamp.
- Package : 2000pcs / reel.
- Moisture sensitivity level : level 3.
- RoHS compliant.

Description

Made with NPN silicon phototransistor chips.

Package Dimensions



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is ± 0.2 (0.0079") unless otherwise noted.
3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.
4. The device has a single mounting surface. The device must be mounted according to the specifications.



Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{BR CEO}	Collector-to-Emitter Breakdown Voltage	30			V	I _C =100uA E _e =0mW/c m ²
V _{BR ECO}	Emitter-to-Collector Breakdown Voltage	5			V	I _E =100uA E _e =0mW/c m ²
V _{CE (SAT)}	Collector-to-Emitter Saturation Voltage			0.8	V	I _C =2mA E _e =20mW/c m ²
I _{CEO}	Collector Dark Current			100	nA	V _{CE} =10V E _e =0mW/c m ²
T _R	Rise Time (10% to 90%)		15		us	V _{CE} = 5V I _C =1mA R _L =1000Ω
T _F	Fall Time (90% to 10%)		15		us	
I _(ON)	On State Collector Current	0.2	0.4		mA	V _{CE} = 5V E _e =1mW/c m ² λ=940nm

Absolute Maximum Ratings at TA=25°C

Parameter	Max.Ratings
Collector-to-Emitter Voltage	30V
Emitter-to-Collector Voltage	5V
Power Dissipation at (or below) 25°C Free Air Temperature	100mW
Operating Temperature	-40°C To +85°C
Storage Temperature	-40°C To +85°C

Note:

1. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

Typical Electro-Optical Characteristics Curves

Fig.1 Collector Power Dissipation vs. Ambient Temperature

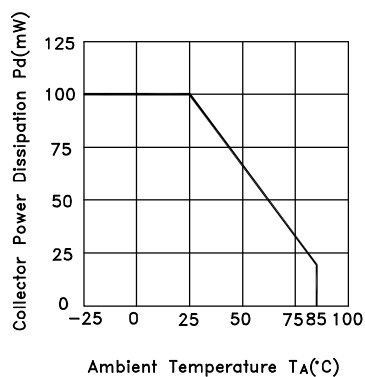


Fig.2 Spectral Sensitivity vs. Wavelength

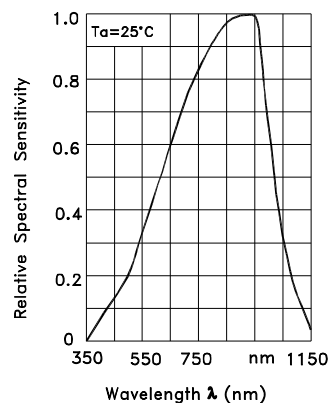


Fig.3 Relative Collector Current vs. Ambient Temperature

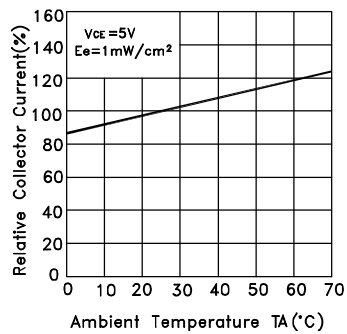


Fig.4 Collector Current $I_c = f(E_e), V_{ce}=5V, T_a=25^\circ C$

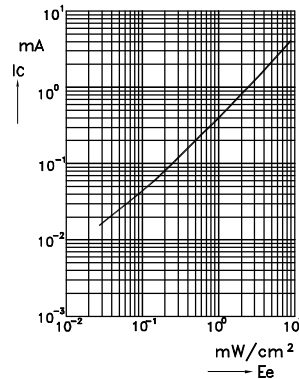


Fig.5 Collector Dark Current vs. Ambient Temperature

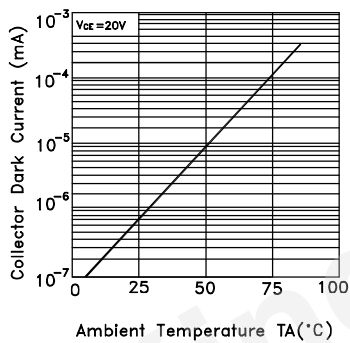


Fig.6 Collector Current vs. Collector-Emitter Voltage

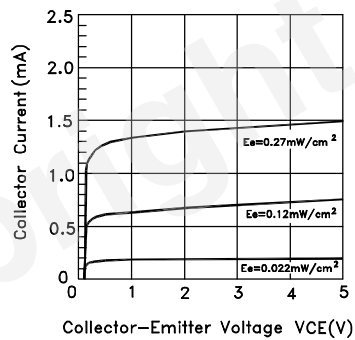
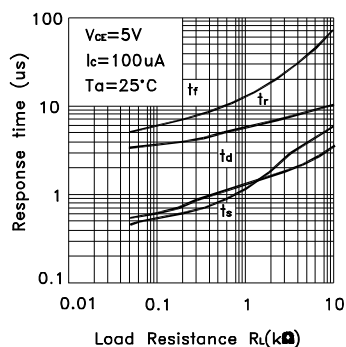
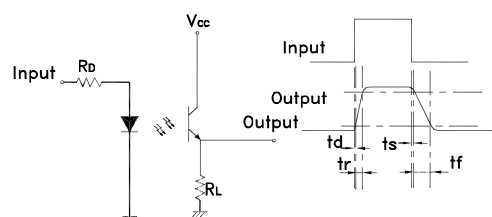


Fig.7 Response Time vs. Load Resistance

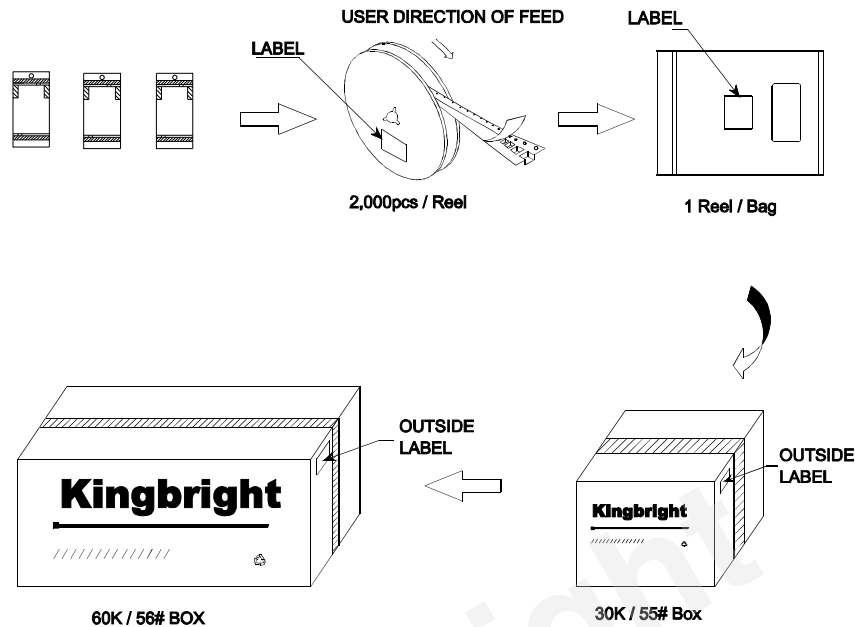



Test Circuit for Response Time



PACKING & LABEL SPECIFICATIONS

KP-3216P3C



Kingbright		
P/NO: KP-3216XXX		
QTY: 2,000 pcs	Q.C.	Q.C.
S/N: XXXX		XXXXXXX PASSED
CODE: XXX		
LOT NO:		
		
RoHS Compliant		

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