

Part Number: L-7113CGCK

Green



**ATTENTION**  
OBSERVE PRECAUTIONS  
FOR HANDLING  
ELECTROSTATIC  
DISCHARGE  
SENSITIVE  
DEVICES

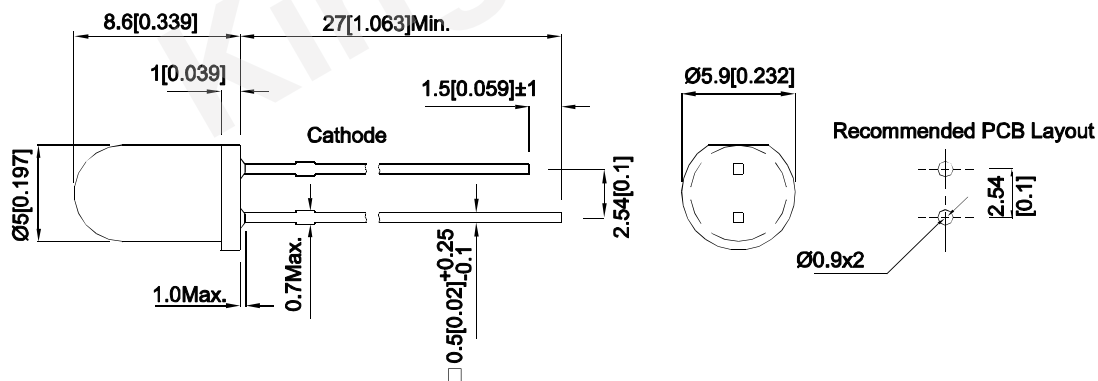
### Features

- Low power consumption.
- Popular T-1 3/4 diameter package.
- General purpose leads.
- Reliable and rugged.
- Long life - solid state reliability.
- Available on tape and reel.
- RoHS compliant.

### Descriptions

- The Green source color devices are made with AlGaInP on GaAs substrate Light Emitting Diode.
- Electrostatic discharge and power surge could damage the LEDs.
- It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs.
- All devices, equipments and machineries must be electrically grounded.

### Package Dimensions



#### Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is  $\pm 0.25(0.01)$  unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



## Selection Guide

Part No.	Emitting Color (Material)	Lens Type	Iv (mcd) [2] @ 20mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
L-7113CGCK	Green (AlGaInP)	Water Clear	400	700	20°

Notes:

1. θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity / luminous Flux: +/-15%.
3. Luminous intensity value is traceable to CIE127-2007 standards.

## Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Emitting Color	Typ.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Green	574		nm	IF=20mA
λD [1]	Dominant Wavelength	Green	570		nm	IF=20mA
Δλ1/2	Spectral Line Half-width	Green	20		nm	IF=20mA
C	Capacitance	Green	15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Green	2.1	2.5	V	IF=20mA
IR	Reverse Current	Green		10	uA	VR = 5V

Notes:

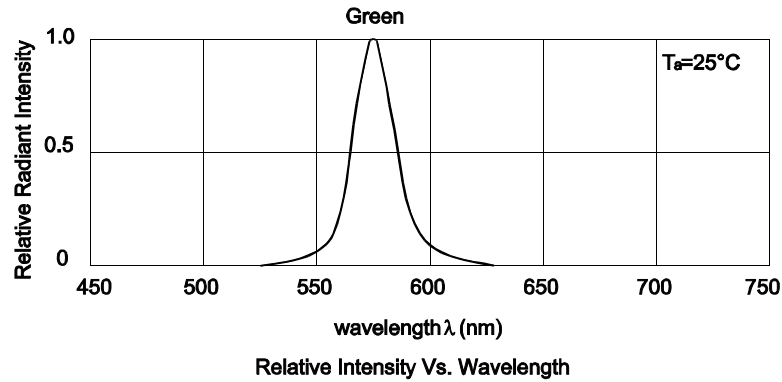
1. Wavelength: +/-1nm.
2. Forward Voltage: +/-0.1V.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

## Absolute Maximum Ratings at TA=25°C

Parameter	Values	Units
Power dissipation	75	mW
DC Forward Current	30	mA
Peak Forward Current [1]	150	mA
Reverse Voltage	5	V
Operating/Storage Temperature	-40°C To +85°C	
Lead Solder Temperature [2]	260°C For 3 Seconds	
Lead Solder Temperature [3]	260°C For 5 Seconds	

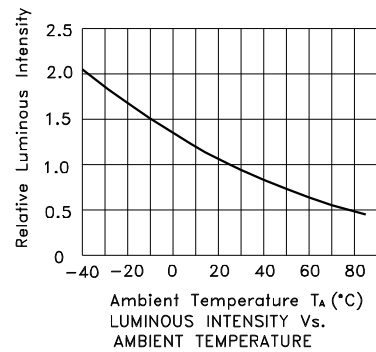
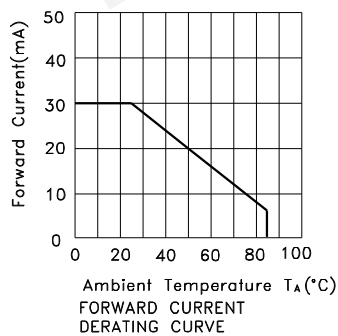
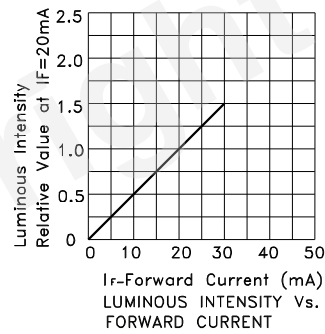
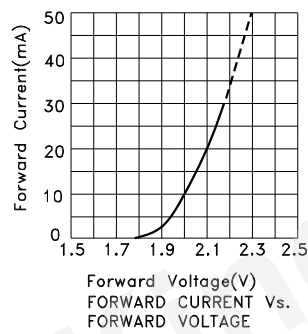
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.



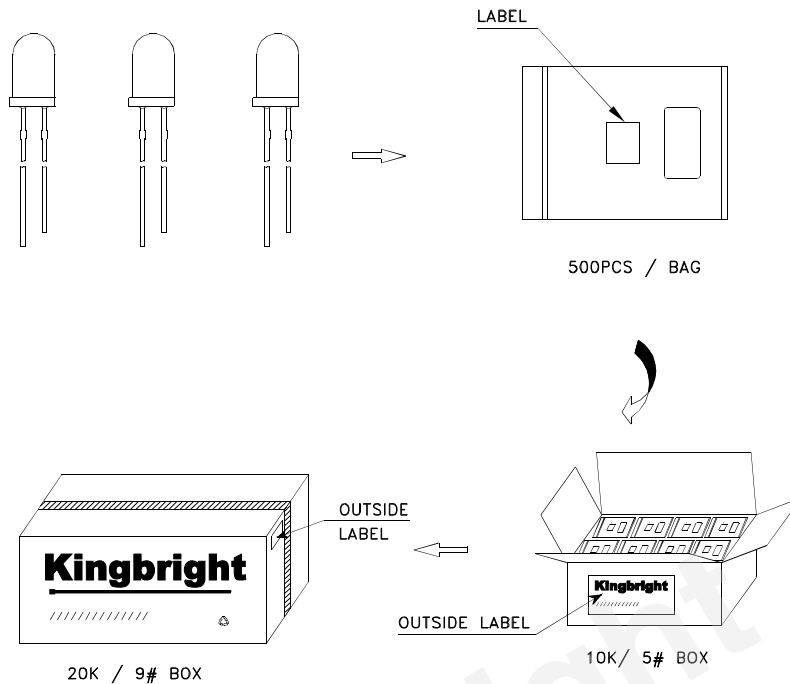
Green

L-7113CGCK



## PACKING & LABEL SPECIFICATIONS

## L-7113CGCK



<b>Kingbright</b>		XXXXXXXXXX-XXXX	
P/NO: XXXXXXXX			
QTY: XXXXpcs			
S/N: XXXX			
CODE: XXX			
COUNTRY: CN		QC DATE: XXX XX XXXX PASSED	
LOT NO:			
XXXXXXXXXX-XXXX			
			1 RoHS Compliant

### Terms and conditions for the usage of this document

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
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## PRECAUTIONS

### 1. Storage conditions:

- Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- LEDs should be stored with temperature  $\leq 30^{\circ}\text{C}$  and relative humidity  $< 60\%$ .
- Product in the original sealed package is recommended to be assembled within 72 hours of opening.  
Product in opened package for more than a week should be baked for 30 (+10/-0) hours at  $85 \sim 100^{\circ}\text{C}$ .

- The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.  
Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)

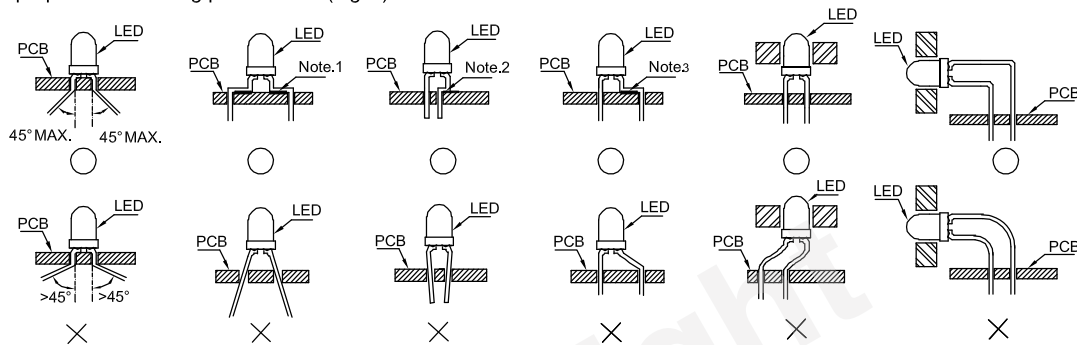


Fig.1

"○" Correct mounting method "X" Incorrect mounting method

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

- When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure. (Fig. 2)

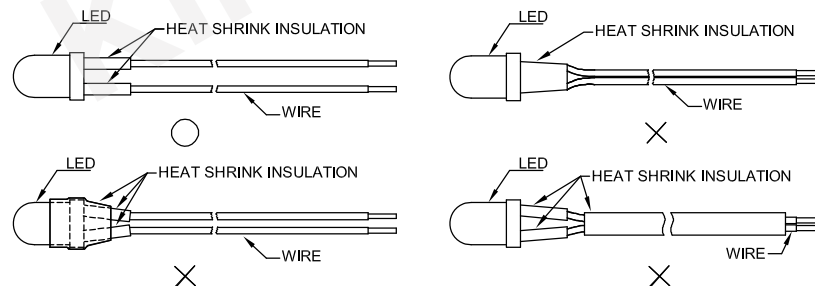


Fig. 2

- Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

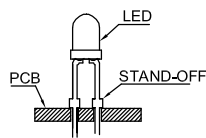


Fig. 3

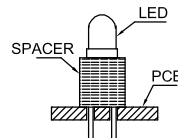


Fig. 4

- Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
- During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

