

**GENERAL PURPOSE
HIGH ISOLATION VOLTAGE
SINGLE TRANSISTOR TYPE
PHOTOCOUPLER SERIES**

FEATURES

- 1.High isolation voltage between input and output (Viso=5000 Vrms)
- 2.Compact dual-in-line package
- KB817-M:1 channel type.
- 3.Long creepage distance type.
- 4.Recognized by UL and CUL, file NO. E225308
5. Approved by VDE 0884 Teil2(NO:40006364)
(Creepage distance between input and output:7mm or more)
6. RoHS Compliant.

DESCRIPTION

- 1.The KB817-M (1-channel) is optically coupled isolators containing a GaAs light emitting diode and an NPN silicon phototransistor.
- 2.The lead pitch is 2.54mm.
- 3.Solid insulation thickness between emitting diode and output phototransistor: $\geq 0.6\text{mm}$.

APPLICATIONS

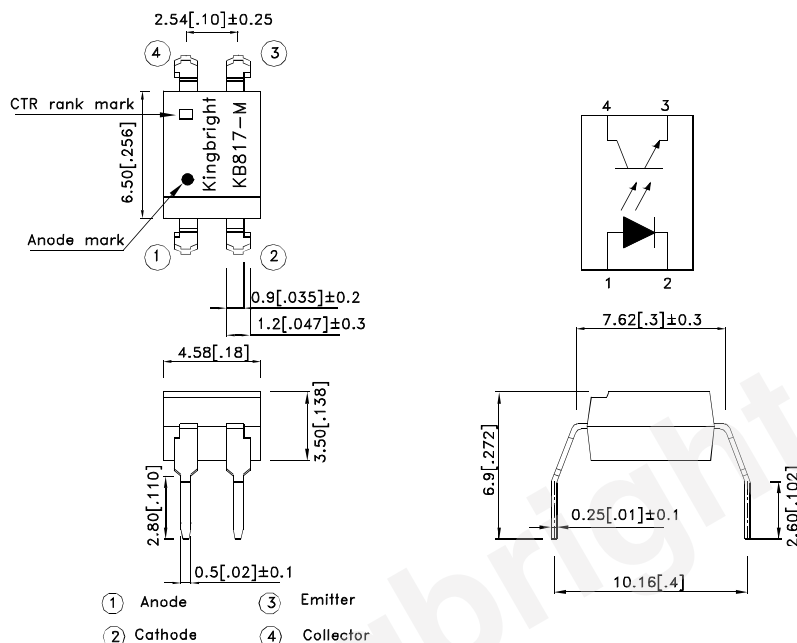
- 1.Computer terminals
- 2.Registers, copiers, automatic vending machines
- 3.System appliances, measuring instruments
- 4.Programmable logic controller
- 5.Signal transmission between circuits of different potentials and impedances



*PACKAGE DIMENSIONS (UNIT:mm)

Lead Bending Type

TOLERANCE : $\pm 0.5[\pm 0.02]$ UNLESS OTHERWISE NOTED.



*Absolute Maximum Ratings (T_A=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward current	I _F	50	mA
	Reverse voltage	V _R	6	V
	Power dissipation	P	70	mW
Output	Collector-emitter voltage	V _{CEO}	35	V
	Emitter-collector voltage	V _{ECO}	6	V
	Collector current	I _C	50	mA
	Collector power dissipation	P _C	150	mW
Total power dissipation		P _{tot}	200	mW
*1 Isolation voltage		V _{iso}	5000	V _{rms}
Operating temperature		T _{opr}	-30~+100	°C
Storage temperature		T _{stg}	-55~+125	°C
*2 Soldering temperature		T _{sol}	260	°C

*1 40 to 60% RH, AC for 1 minute.

*2 For 10 seconds.

*3 Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

* Electro-optical Characteristics (TA=25°C)

Parameter		Symbol	Conditions	Min.	Typ.	Max.	Unit
Input	Forward voltage	V_F	$I_F=20mA$	—	1.2	1.4	V
	Peak forward voltage	V_{FM}	$I_{FM}=0.5A$	—	—	3.0	V
	Reverse current	I_R	$V_R=4V$	—	—	10	μA
Output	Collector dark current	I_{CEO}	$V_{CE}=20V, I_F=0mA$	—	—	10^{-7}	A
Transfer characteristics	*1 Current transfer ratio	CTR	$I_F=5mA, V_{CE}=5V$	50	—	600	%
	Collector-emitter saturation voltage	$V_{CE(Sat)}$	$I_F=20mA, I_C=1mA$	—	0.1	0.2	V
	Cut-off frequency	f_c	$V_{CE}=5V, I_C=2mA$ $R_L=100\Omega, -3dB$	—	80	—	kHz
	Response time	Rise time	$V_{CE}=2V, I_C=2mA$ $R_L=100\Omega$	—	4	18	μS
		Fall time		—	3	18	μS

*1 Classification table of current transfer ratio is shown below.

$$CTR = \frac{I_C}{I_F} \times 100\%$$

Model No.	Rank mark	CTR (%)
KB817L-M	L	50 to 100
KB817A-M	A	80 to 160
KB817B-M	B	130 to 260
KB817C-M	C	200 to 400
KB817D-M	D	300 to 600
KB817AB-M	A or B	80 to 260
KB817BC-M	B or C	130 to 400
KB817CD-M	C or D	200 to 600
KB817AC-M	A,B or C	80 to 400
KB817BD-M	B,C or D	130 to 600
KB817AD-M	A,B,C or D	80 to 600
KB817-M	L,A,B,C,D or No mark	50 to 600

Fig. 1 Current Transfer Ratio vs. Forward Current

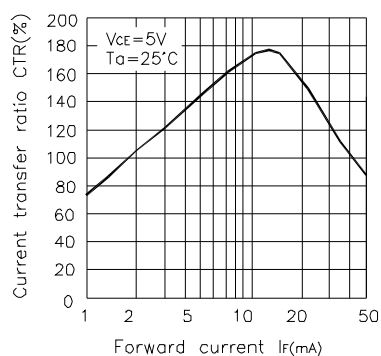


Fig. 2 Forward Current vs. Forward voltage

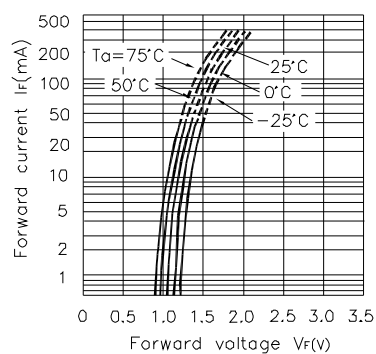


Fig. 3 Collector Current vs. Collector-emitter Voltage

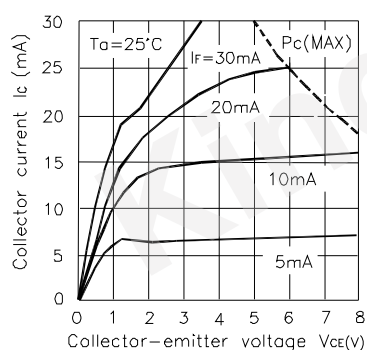


Fig. 4 Relative Current Transfer Ratio vs. Ambient Temperature

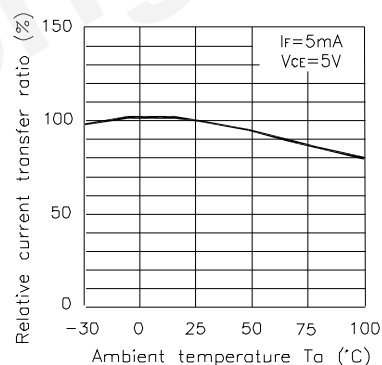


Fig. 5 Collector-emitter Saturation Voltage vs. Ambient Temperature

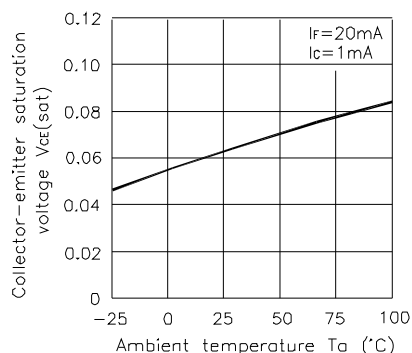


Fig. 6 Collector Dark Current vs. Ambient Temperature

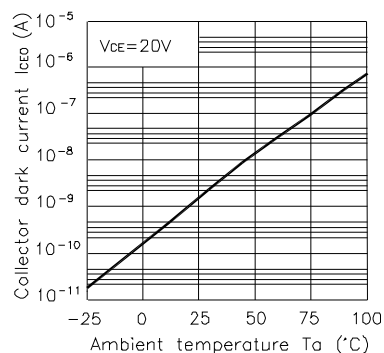


Fig. 7 Forward Current vs. Ambient Temperature

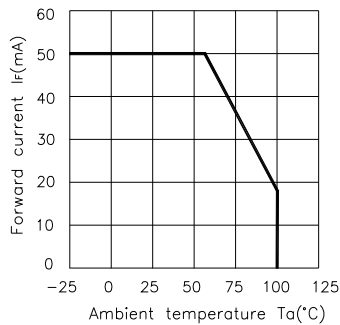


Fig. 8 Collector Power Dissipation vs. Ambient Temperature

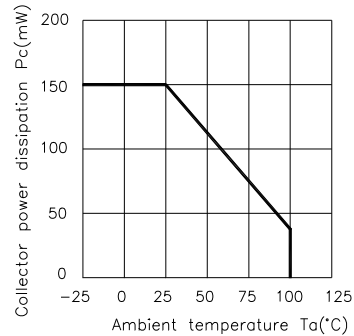
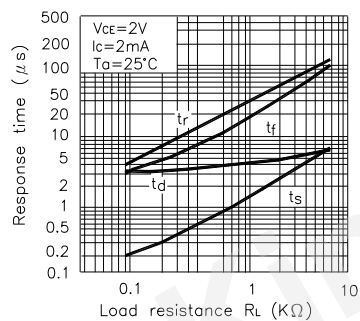


Fig. 9 Response Time vs. Load Resistance



Test Circuit for Response Time

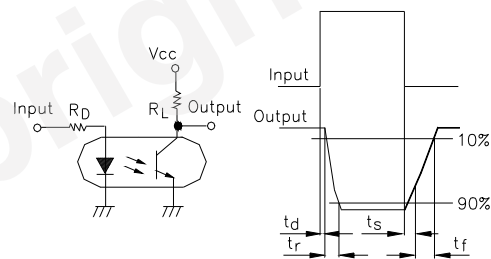
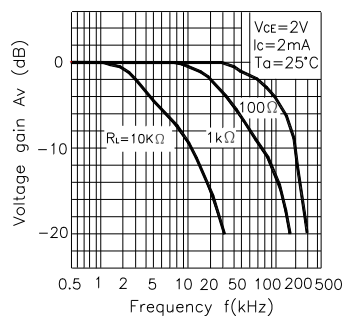


Fig. 10 Frequency Response



Test Circuit for Frequency Response

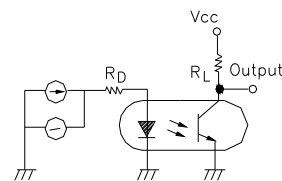
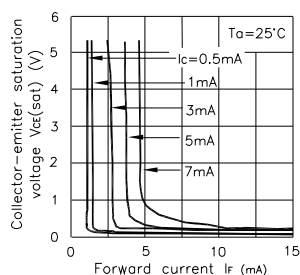


Fig. 11 Collector-emitter Saturation Voltage vs. Forward Current

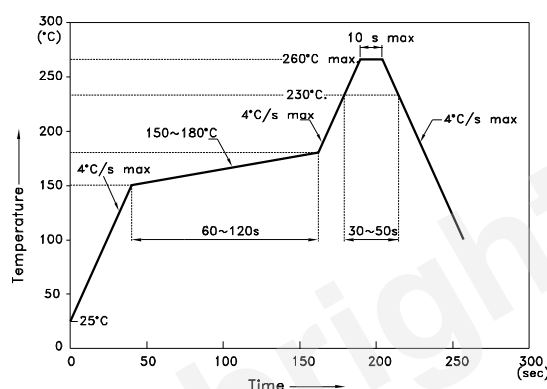


*NOTES ON HANDLING

1.Cautions regarding noise

Be aware that power is suddenly into the component any surge current may cause damage happen, even if the voltage is within the absolute maximum ratings.

Reflow Soldering Profile For Lead-free SMT Process.



NOTES:

- 1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C.
- 2.Don't cause stress to the epoxy resin while it is exposed to high temperature.
- 3.Number of reflow process shall be 2 times or less.

CAUTION

Within this device there exists GaAs (Gallium Arsenide) material which is a harmful substance if ingested. GaAs dust and fumes are toxic. Do not break, cut or pulverize the product, or use chemicals to dissolve them.

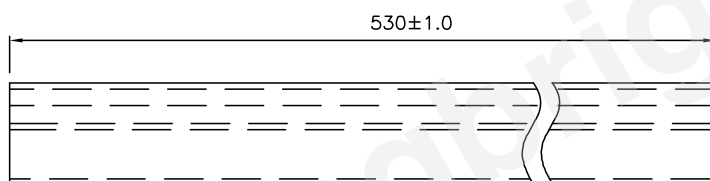
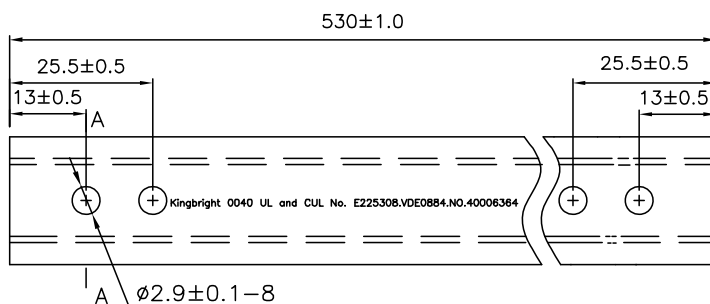
RESTRICTIONS ON PRODUCT USE

- The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices / types available in every country.
- We are mention about our product quality stability, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing KINGBRIGHT products, to observe standards of safety, and to a avoid situations in which a malfunction or failure of a KINGBRIGHT product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that KINGBRIGHT products are used within specified operating ranges as set forth in the most recent products specifications.

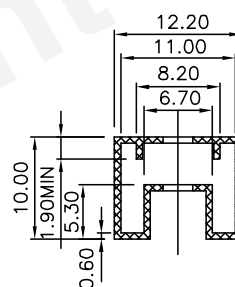
KB817-M

Dimension of Tube

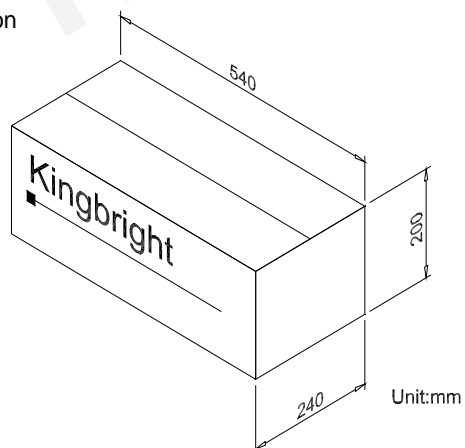
TOLERANCE : $\pm 0.4[\pm 0.012]$ UNLESS OTHERWISE NOTED.
Unit:mm



A-A Side view



Dimension of Carton

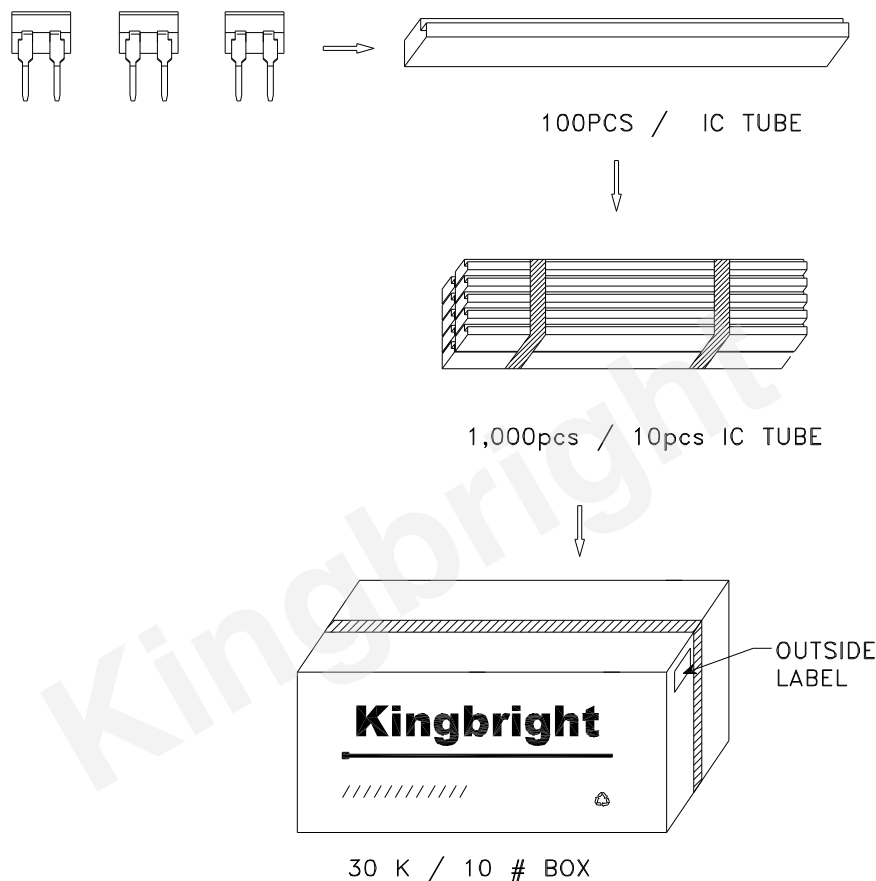



*ORDERING INFORMATION

Part Number	Package	Package Style
KB817-M	4-pin DIP	100pcs/each tube

Part Number: KB817-M

PACKING & LABEL SPECIFICATIONS



Kingbright	
P/NO: KB817-M	
QTY: 1,000 pcs	Q.C. XX XX XXXX PASSED
S/N: XXXX	
CODE: XXX	
LOT NO:	
	
RoHS Compliant	